To DCT-EDP Debug Port

P1

VDD	1	2	TDI
SL	3	4	TDO
	5	6	TCK
CLK1	7	8	TMS

Header 4X2

U1

VCC3V3_A

DC6288FT32N3E

1	TDI
2	TDO
3	PB4
4	RSTN
5	PC2
6	PC1
7	VSS
8	PC0
9	VDD
10	PA0

TMS	20
TCK	19
PB1	18
PA7	17
PA6	16
PA5	15
PA4	14
PA3	13
PA2	12
PA1	11

ECLK

VDD

SL

VCC3V3_A

C1 220uF   C2 0.1uF

SL Programming Port

P2

1	VDD
2	SL
3	
4	ECLK

Header 4

Remarks:
1. Programming using DCT-EDP refer DCT-EDP manual which is available in our DC6288 emulator/programming website.
   Programming using DCT-SLP or DC6688SLP Rev3.2 refer its manual which is available in our programmer website.
2. Precautiion on programming pins refer AppNote300 for detail.
3. RSTN pin should be no connection if the application do not need reset.
   If the application need external MCU reset, refer AppNote300 for detail.

Title	*DC6288FT32N3E ref. circuit*	
Size	Number	Revision
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Date:	10/1/2020	Sheet of
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