Dragonchip

DC6388EVK User Manual

Document Revision 1.2

December, 2018

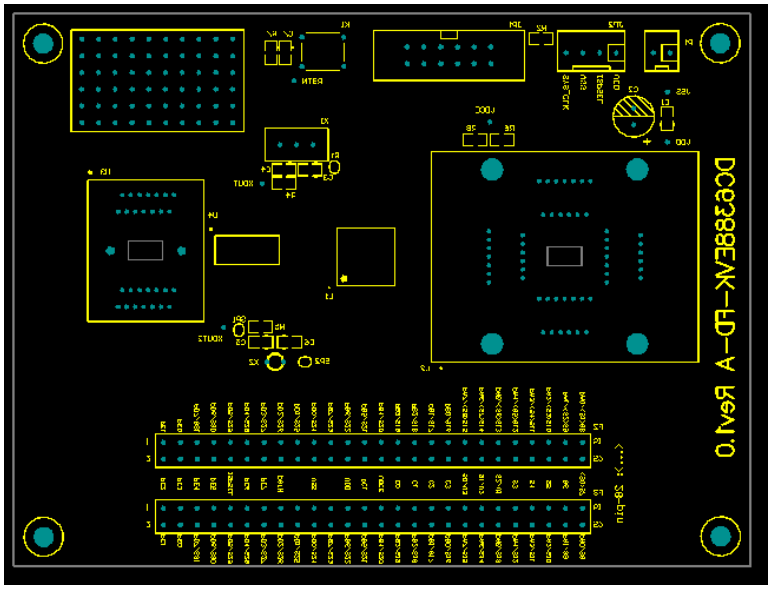
Contents

1 Introduction

The Objective of this document is to provide a quick start to use the development tool DC6388EVK which supports the DC6388F family.

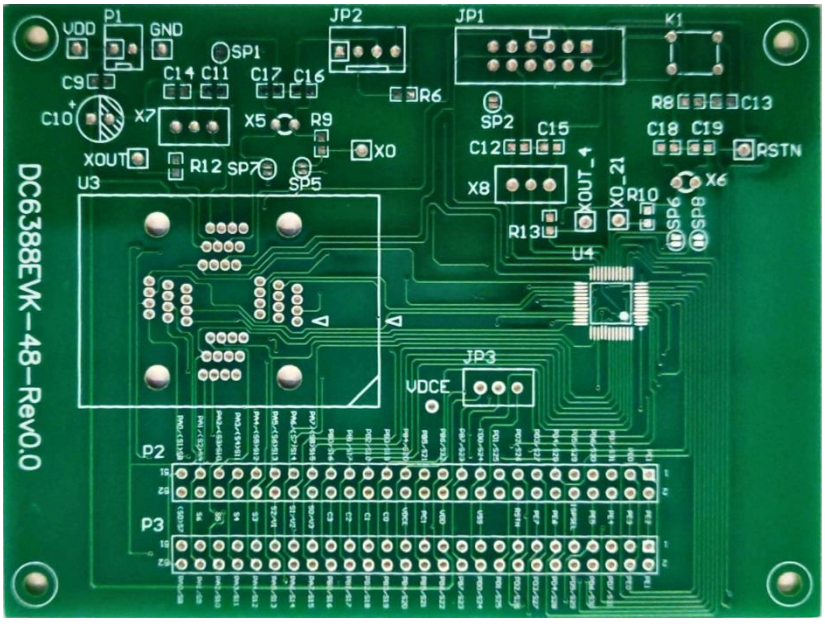
1.1 Ordering Part Number

- DC6388EVK-FD-LQFP52



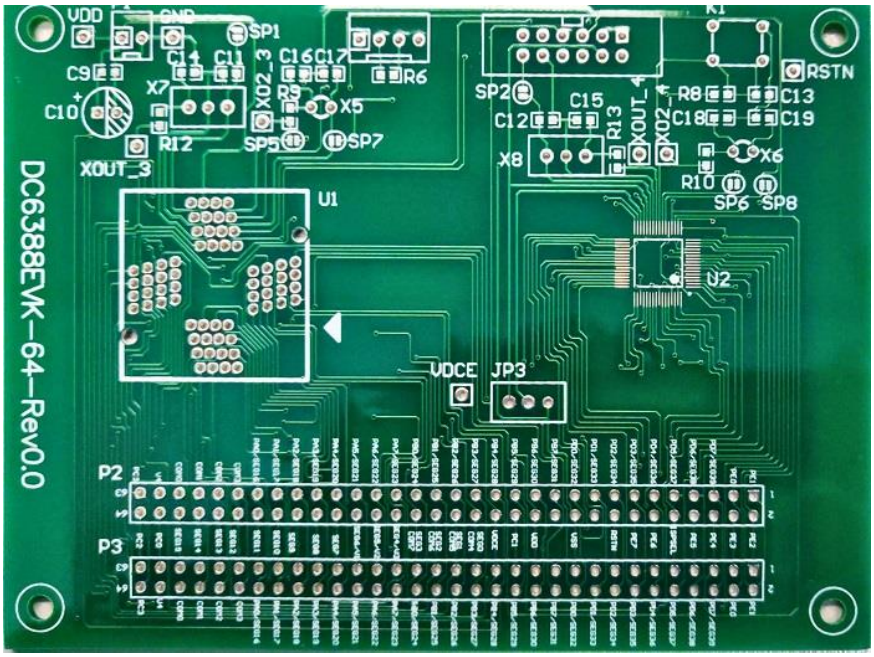
This board contains IC socket which supports DC6388FD in LQPF52 package.

- DC6388EVK-48



This board contains IC socket which supports DC6388FD in LQPF48 package.

- DC6388EVK-64



This board contains IC socket which supports DC6388FD in LQPF64 package.

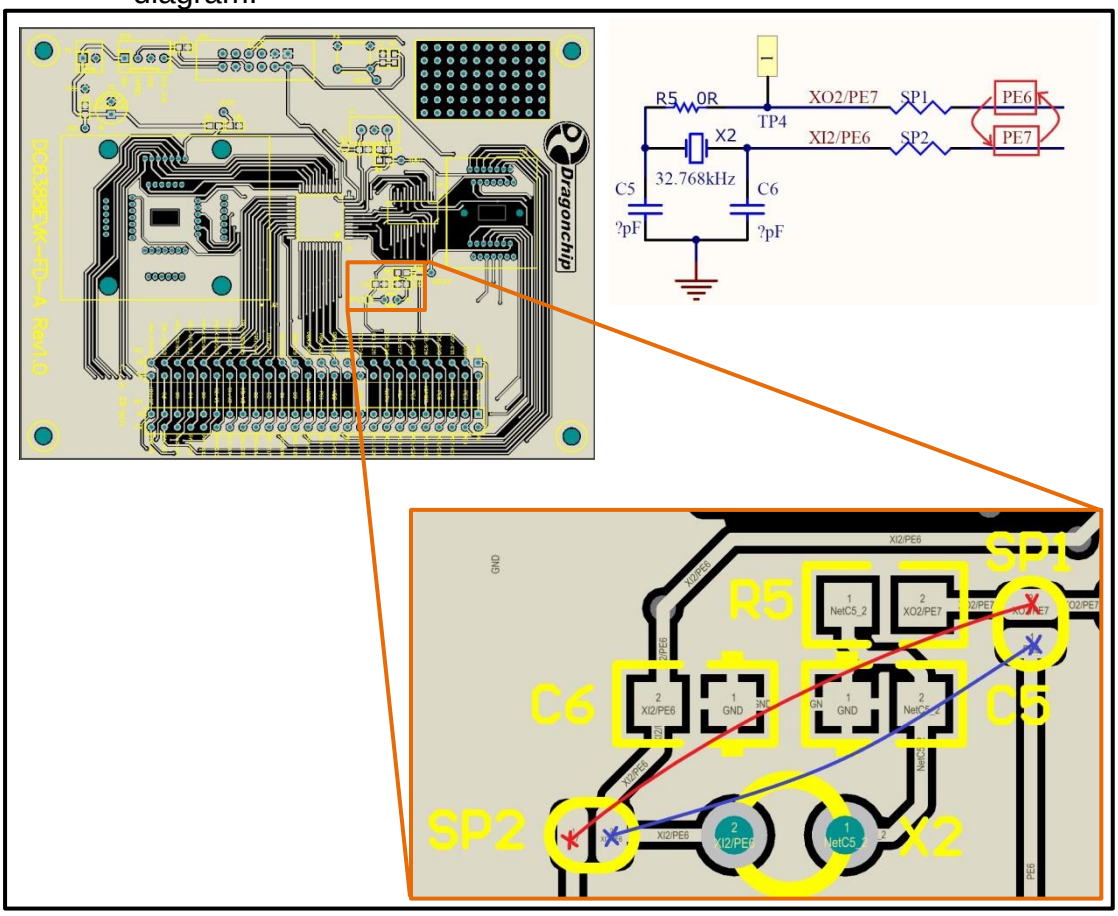
2 Short Point Setting

2.1 XIN Oscillator

1. When on-board resonator (X1) is used as XIN Oscillator source, do following setting:
   - DC6388EVK-FD-LQFP52
     Open the short point ‘R1’
   - DC6388EVK-48 / 64
     Open the short point ‘SP1’ / ‘SP2’
2. When external clock is used as XIN Oscillator source, connect the clock source to the pin ‘SYS_CLK’. Setting:
   - DC6388EVK-FD-LQFP52
     Short the short point ‘R1’
   - DC6388EVK-48 / 64
     Short the short point ‘SP2’
     Open the short point ‘SP1’

2.2 XIN2 Oscillation

1. When XIN2 Oscillator is not in used, both PE6 and PE7 can be used as GPIO. Follow this setting to connect the PE6 and PE7 on IC to the pin headers P2 and P3.
   - DC6388EVK-FD-LQFP52
     Make wire connection as shown in red and blue lines in the following diagram:



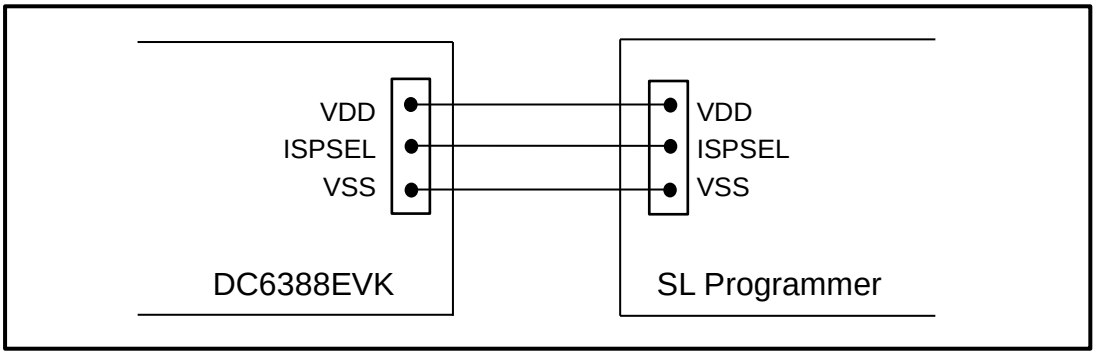
   - DC6388EVK-48 / 64
     Short the short point ‘SP5’ and ‘SP7’

2. When XIN2 Oscillator is in used, both PE6 and PE7 are disabled. Follow this setting:
   - DC6388EVK-FD-LQFP52
     Open the short points ‘SP1’ and ‘SP2’
   - DC6388EVK-48 / 64
     Open the short point ‘SP5’ and ‘SP7’

3 Programming

DC6388F IC can be programmed by Single Line Programmer (DC6688SLP-USB). To program the IC, connect the 3-pin connector from the programmer to the programming port of the EVK board. The 3 pins used for SL programming are VSS, ISPSEL and VDD.

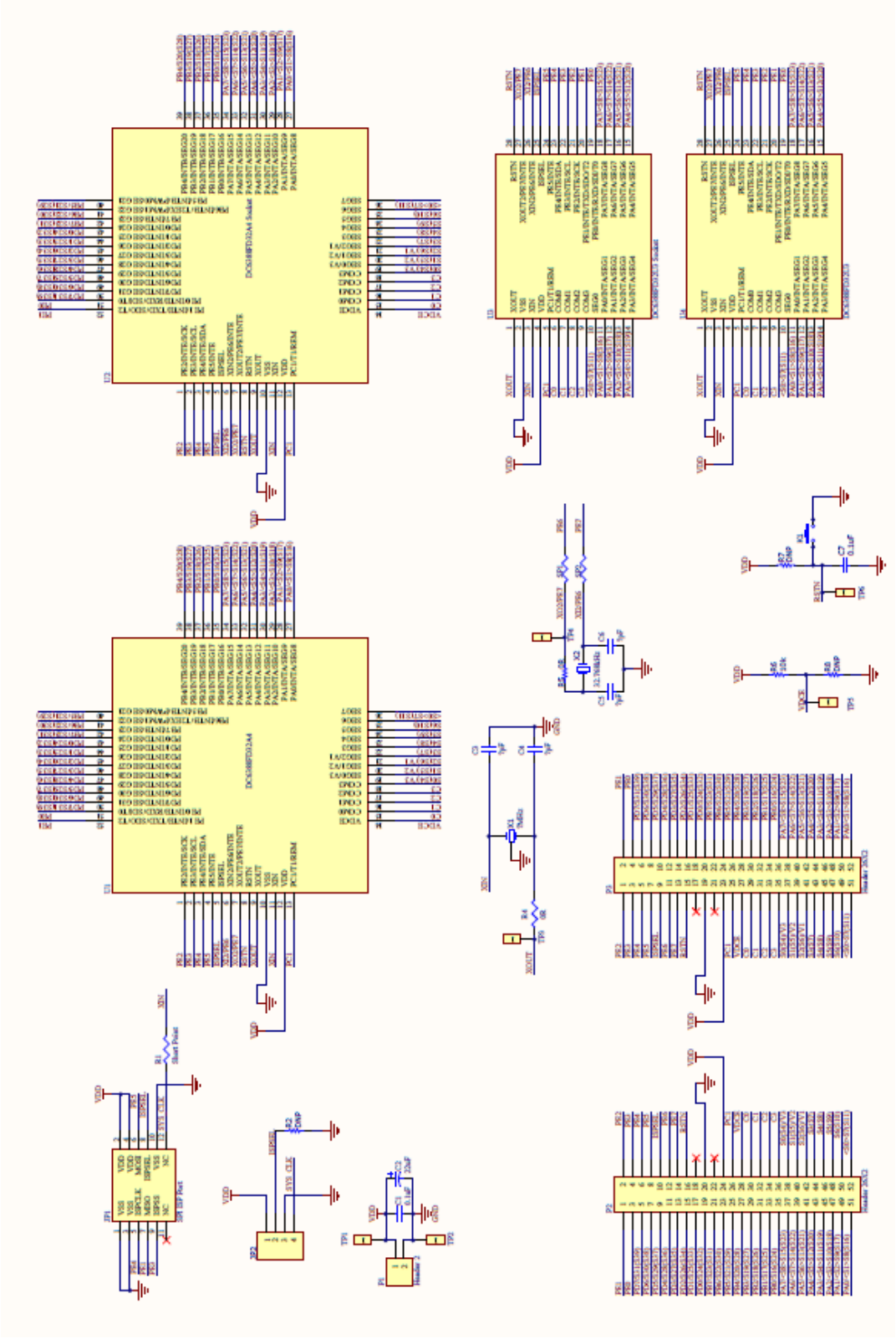
- DC6388EVK-FD-LQFP52
  Programming port: JP2
- DC6388EVK-48 / 64
  Programming port: JP2



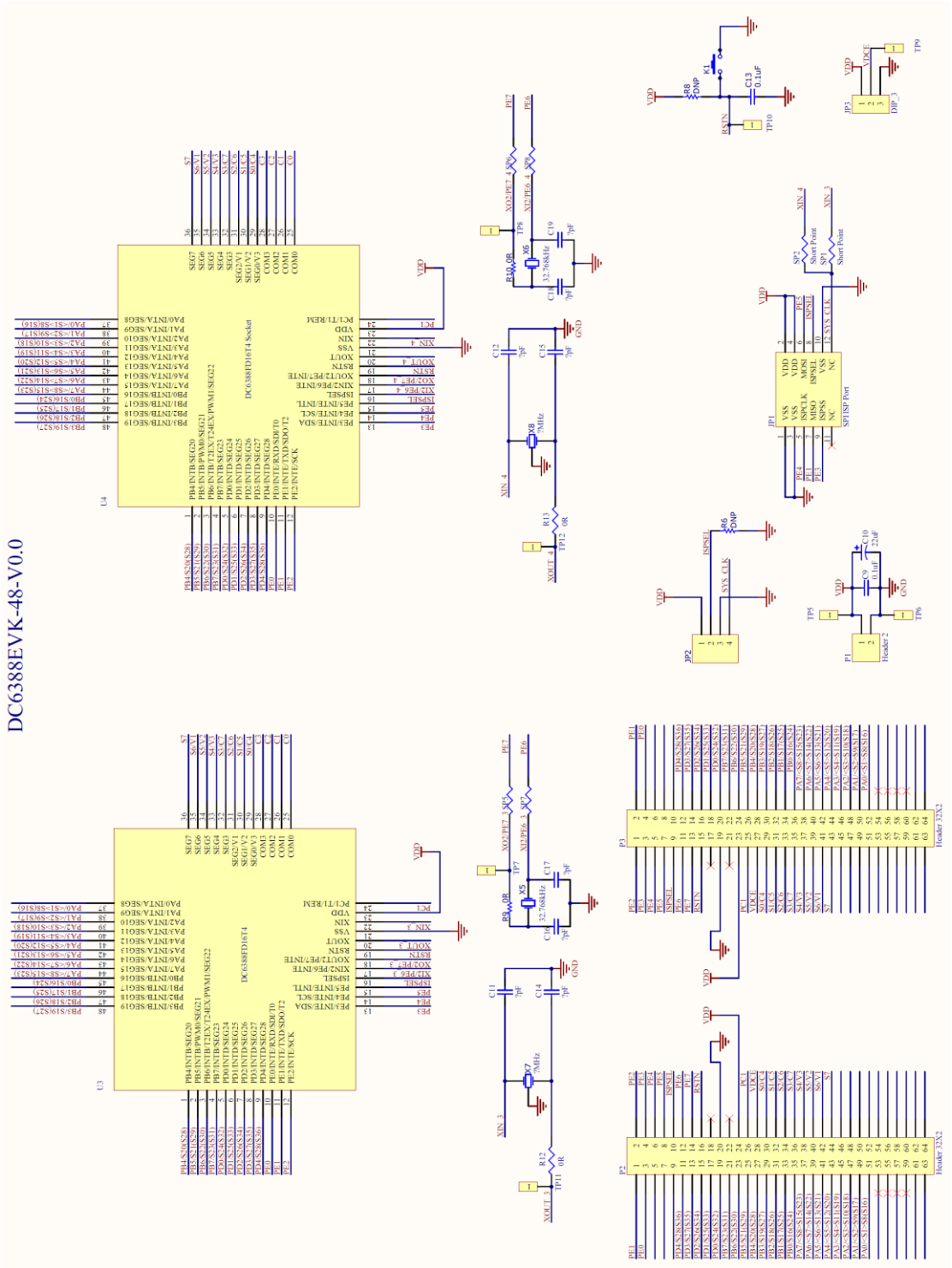
Download the latest SLP user manual for more details:
http://www.dragonchip.com/TechDoc/DevelopmentTools/SLP.htm

4 Schematics

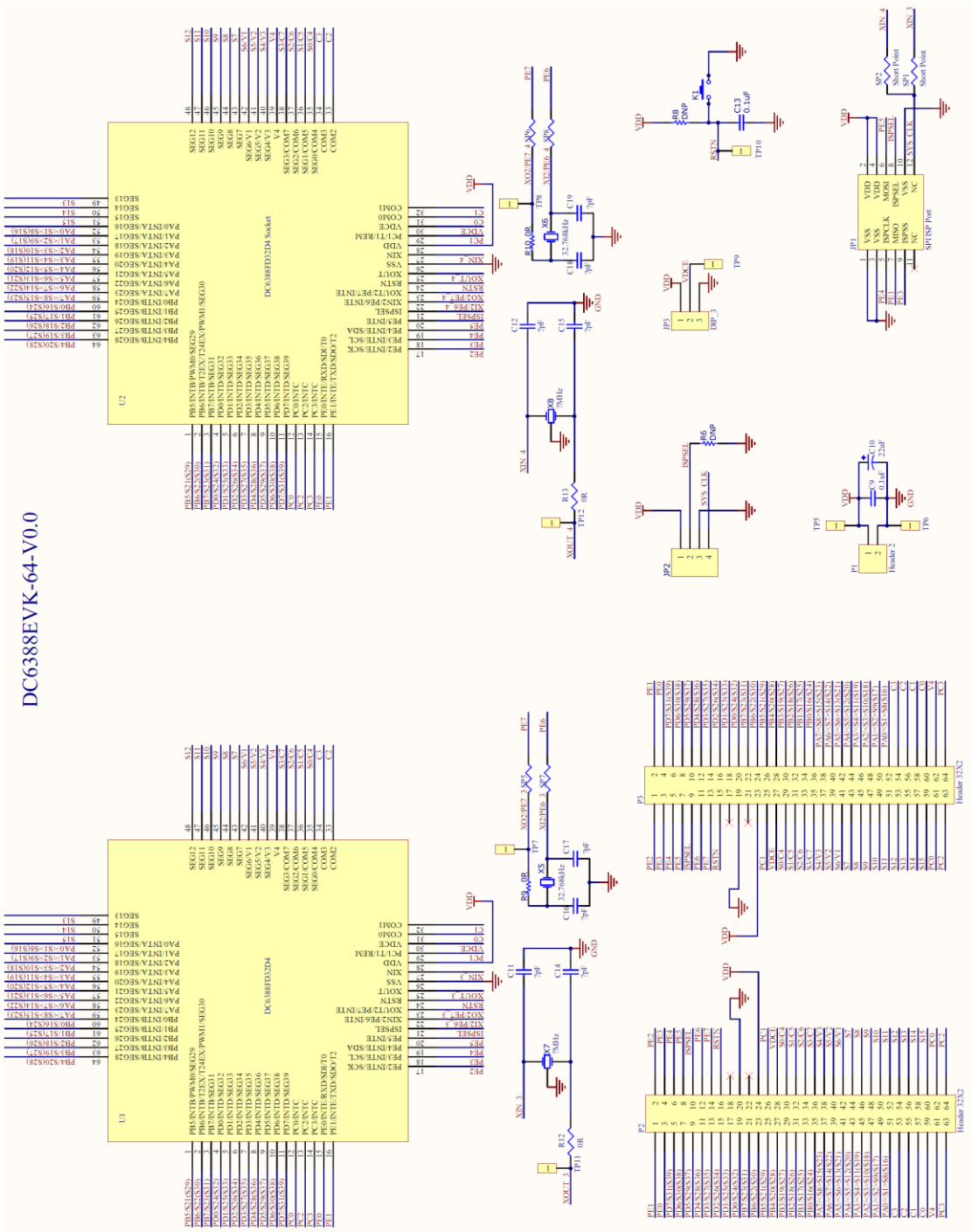
DC6388EVK-FD



DC6388EVK-48



DC6388EVK-64



5 Revision History

Document Rev. No.	Issued Date	Section	Page	Description	Edited By	Reviewed By
1.0	Sep, 2014	All		First Release	Philip Hung	Danny Ho
1.1	Nov, 2014	2.2 3		Add wire connection information Revise the format	Celia Ki	Danny Ho
1.2	Dec, 2018	All		Add DC6388EVK-48 / DC6388EVK-64	Danny Ho	Patrick Li

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