



Dragonchip

DC6688F2R-EVK Manual

Document Revision 1.0

September, 2013

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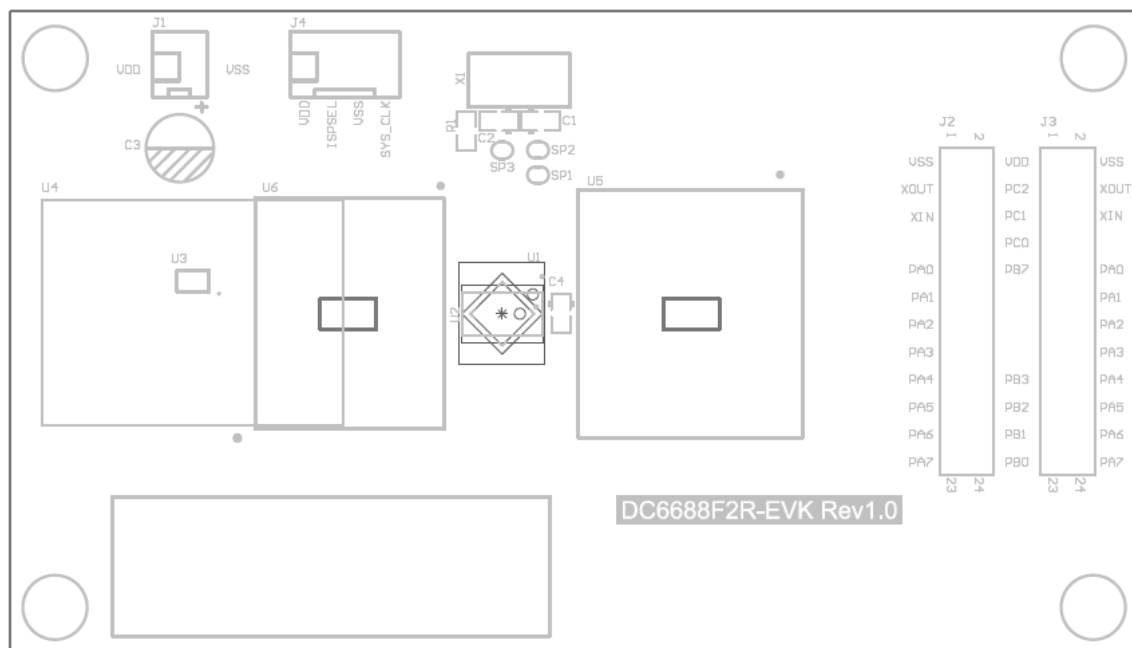
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1 Introduction

The Objective of this document is to provide the user a quick start to use DC6688F2R-EVK to download code to device. This board is applicable to DC6688F2 series.

For whole software and hardware setup to download code to device, user can refer to the manual of DC6688SLP-USB Rev3.0 or higher.

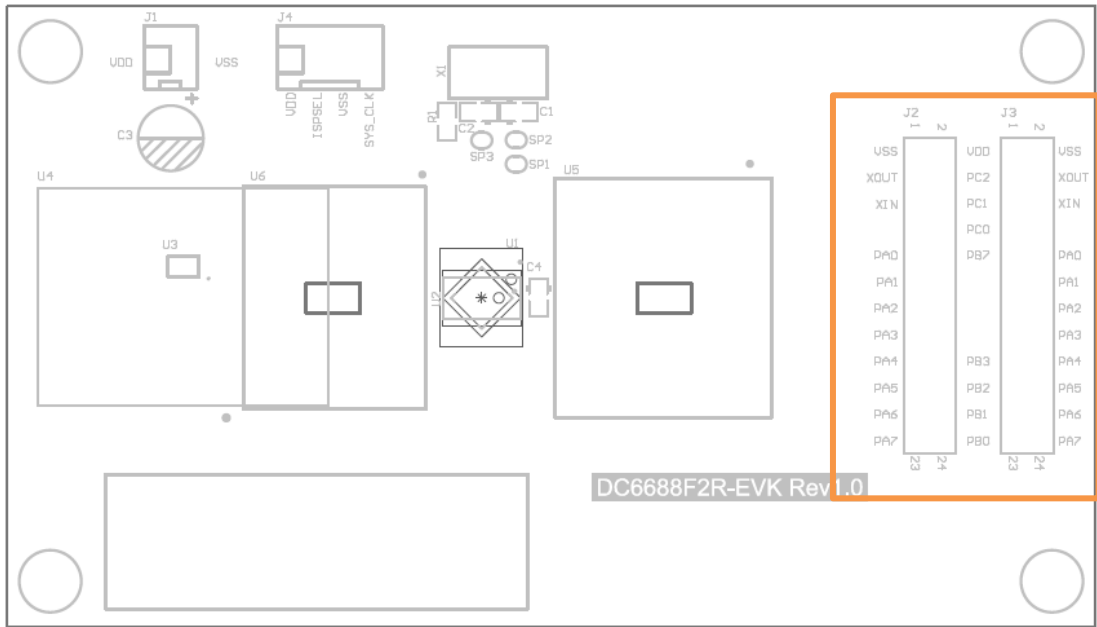
1) DC6688F2R-EVK-Revx.y



DC6688F-EVK-SOP supports 20pin SSOP / TSSOP package.

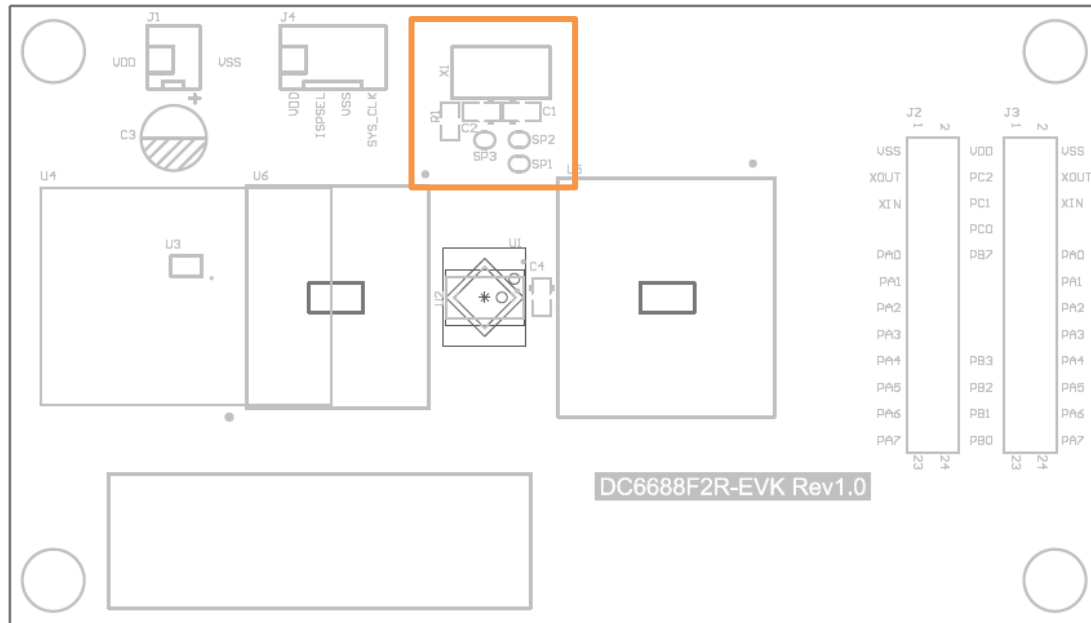
2 Pin Header to Target board

The board provides connector to customer target board as shown below.



3 Resonator

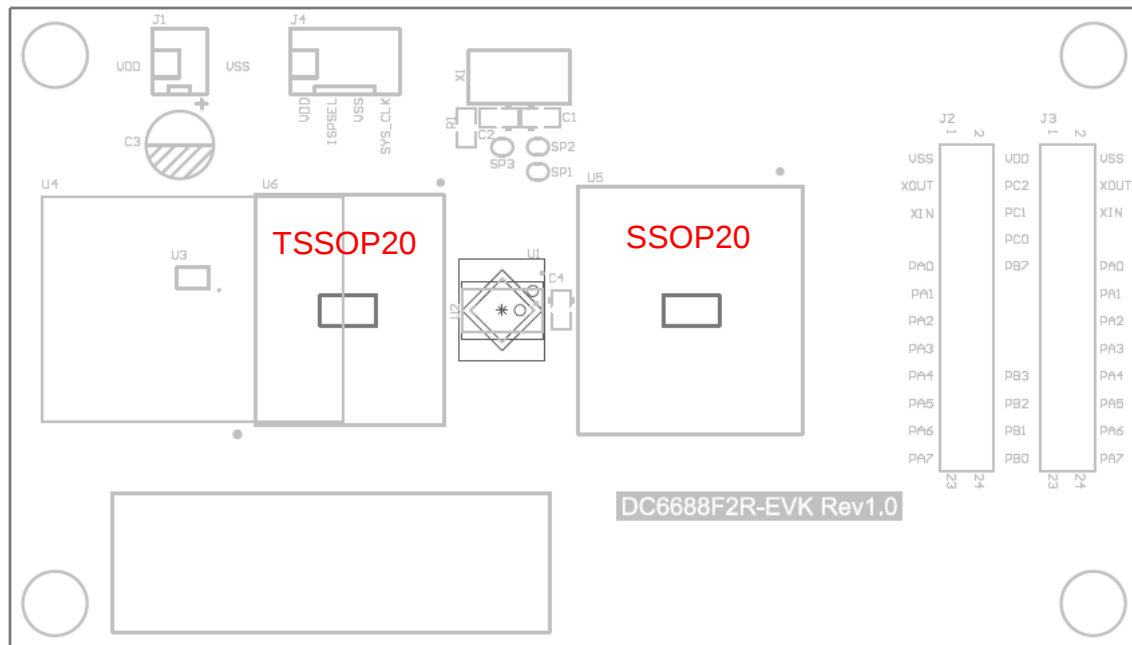
The board is installed with 4MHz by default where no feedback and damping resistor is required. Other frequency requires matching report. Damping resistor R1 pad is provided for this purpose.



4 IC placement

1) SSOP20

The socket for SSOP20 is shown below. The white dot marking on PCB shows the orientation of IC placement.



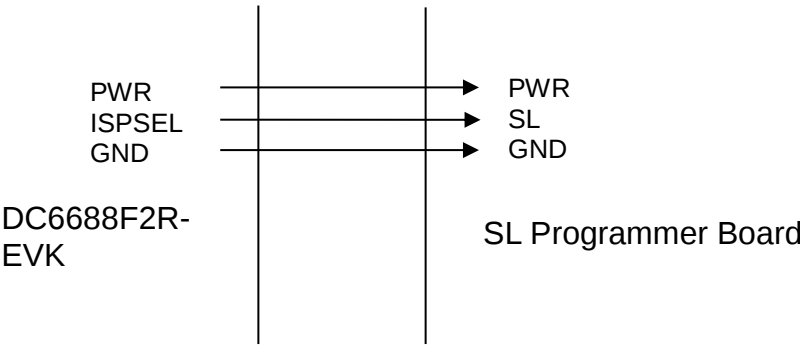
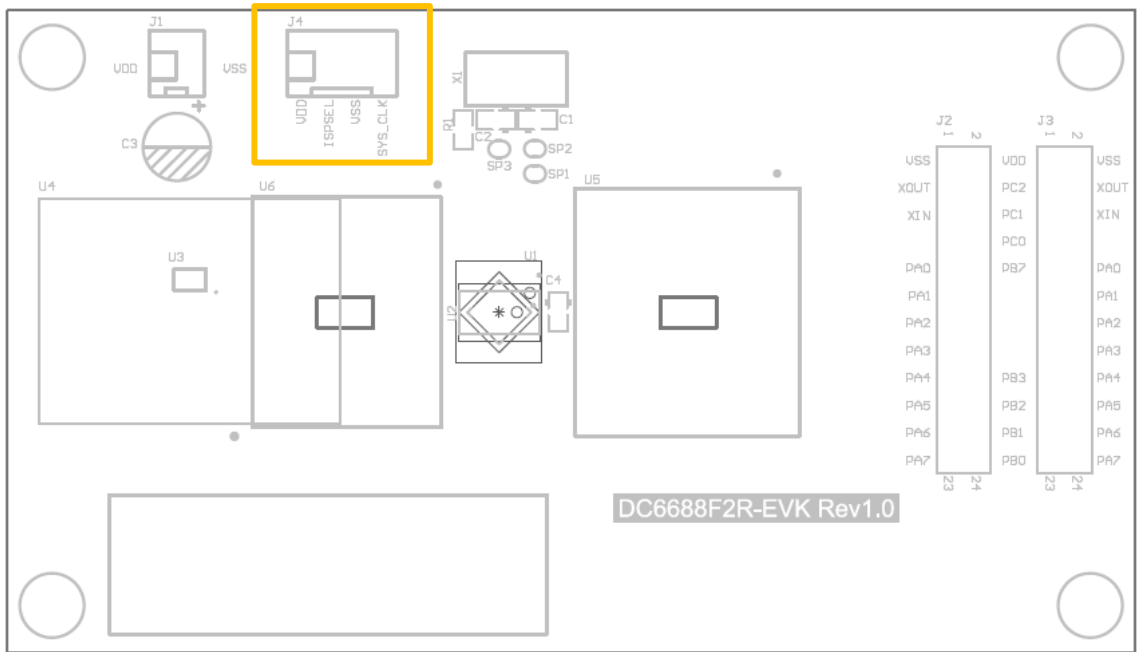
2) TSSOP20

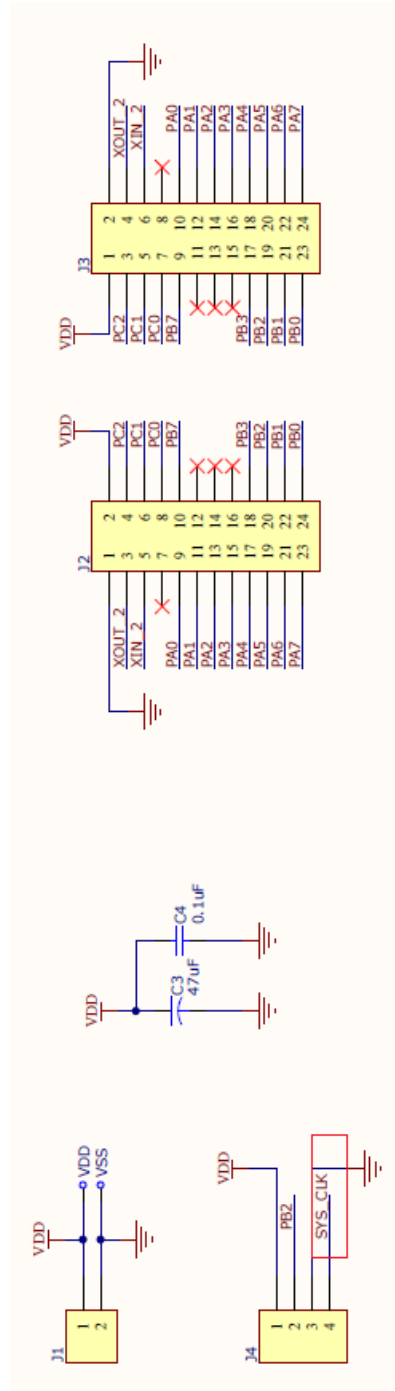
The socket for TSSOP20 is shown above. The white dot marking on PCB shows the orientation of IC placement.

5 SL Programming

1) DC6688F2R-EVK

To program the IC with Single Line (SL) Programmer Board (Manual for SL Programmer Board should be referred), a 3-pin connector from the programmer board should be plugged into J4 (or J11). The 3 pins used for SL programming are GND, ISPSEL and VCC:





Revision History

Document Rev. No.	Issued Date	Section	Page	Description	Edited By	Reviewed By
1.0	Sept, 2013			Preliminary	Danny Ho	Celia Ki

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